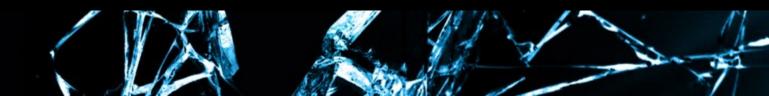
Brute-Forcing Lockdown Harddrive PIN Codes



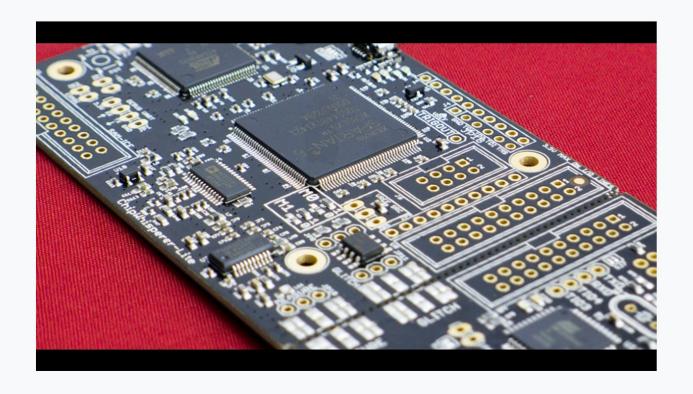
Colin O'Flynn





ABOUT ME

ChipWhisperer-Lite: A New Era of Hardware Security Research



Embedded security - is it an oxymoron? Learn the truth through a series of hands-on labs targeting computer and electrical engineers.

Created by

Colin O'Flynn



331 backers pledged \$88,535 CAD to help bring this project to life.

TYPES OF SECURE DRIVES







PREUZOUS WORK

Joffrey Czarny & Raphaël Rigo

Presentation at Hardwear.io:

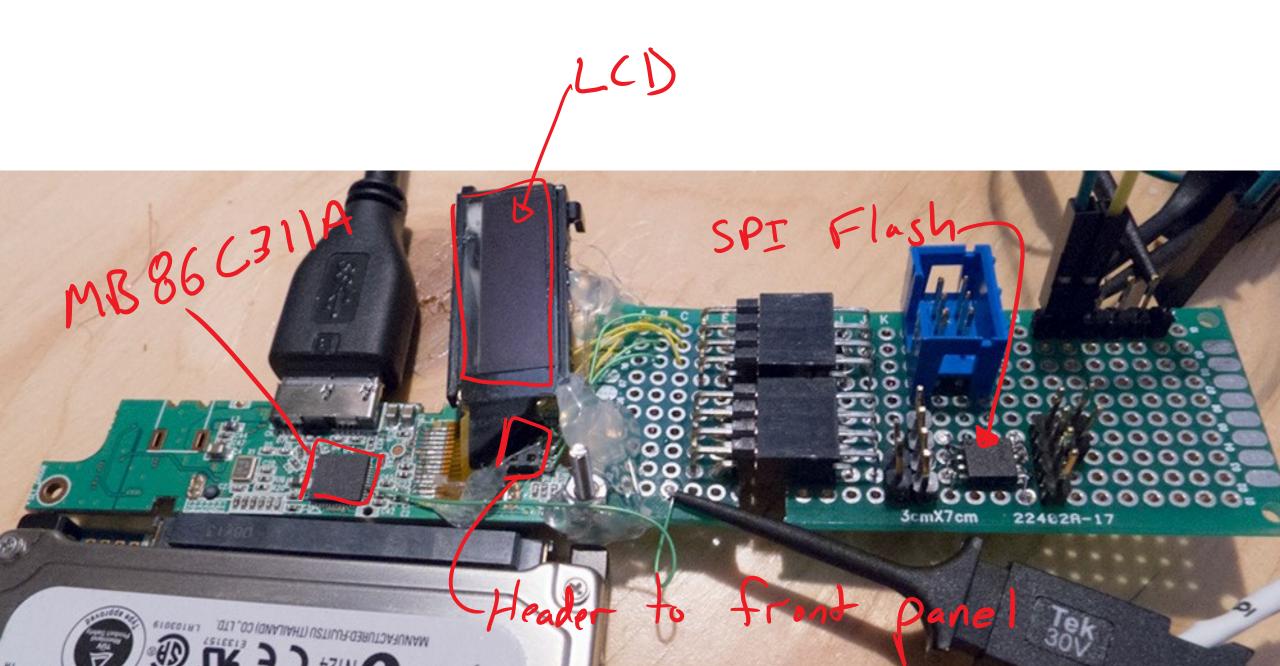
http://hardwear.io/wp-content/uploads/2015/10/Slide-hardware_re_for_software_reversers-By-Czarny-Rigo.pdf Lots of details in paper:

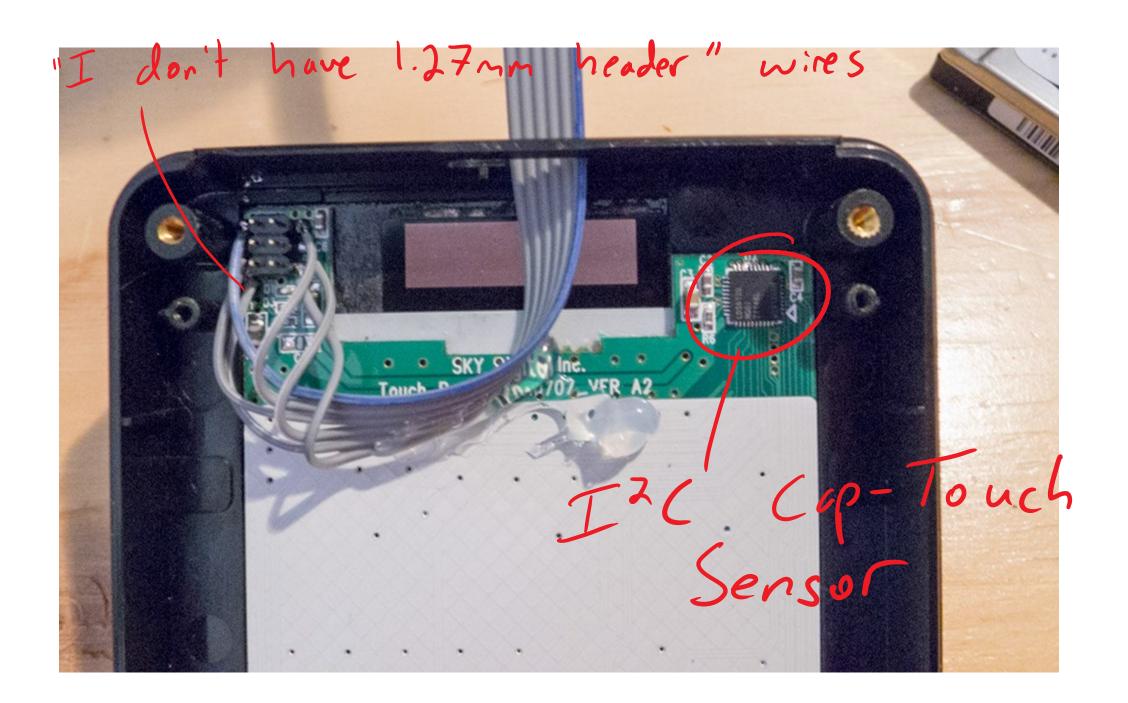
https://www.sstic.org/media/SSTIC2015/SSTIC-actes/hardware_re_for_software_reversers/SSTIC2015-Article-

hardware_re_for_software_reversers-czarny_rigo.pdf

Me - Dayer - Czarny & Rigo's Paper - Czarny



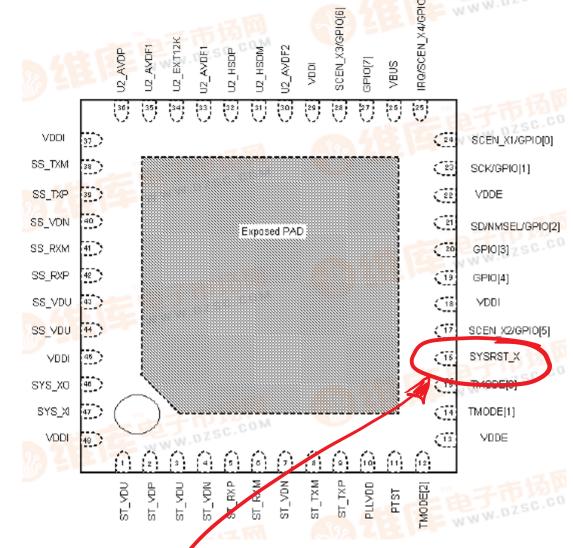




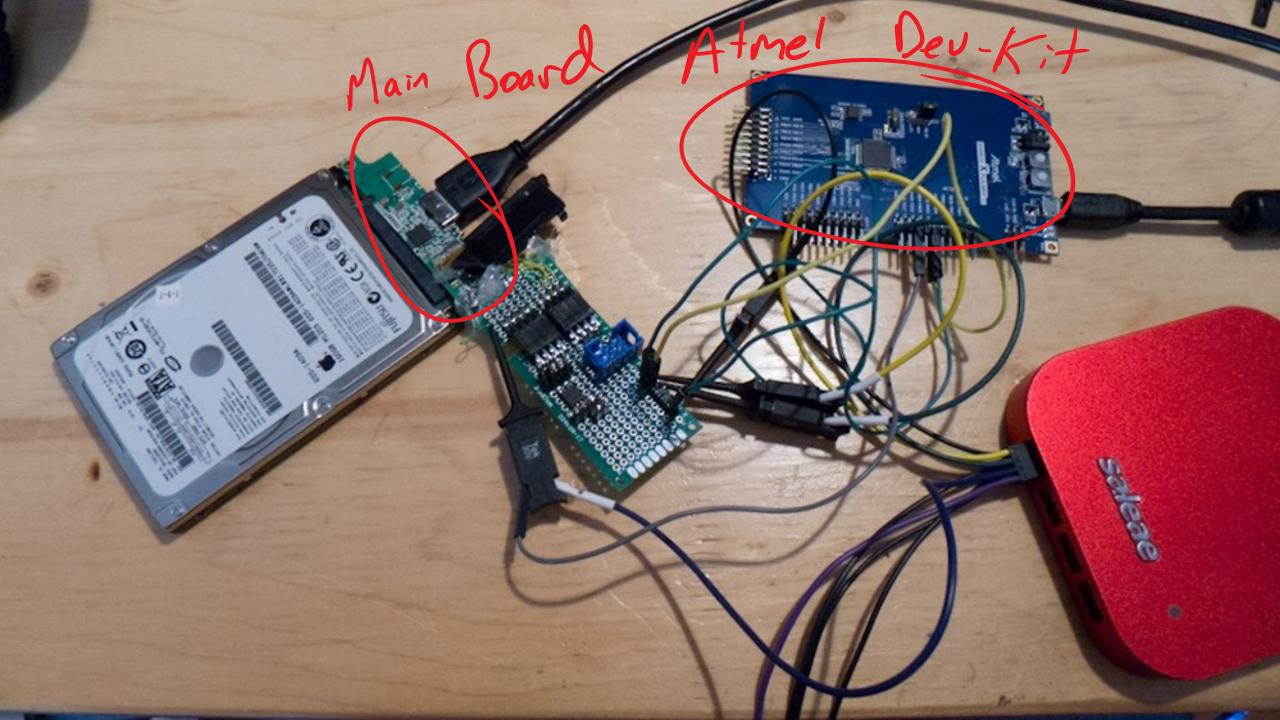
15 LO(K 1) JAGRAM LDS6100 MB 86 C 3/1A MSB 3.07 I2C Bus AES Peripheral SPI FLASH HD SATA

3. Turning on/off the power supply

| o. running off on the power supply | | | | | | | |
|--|-------------------------|---|----------------------|--|--|--|--|
| item | | Pin | | | | | |
| Turning on/ off the power supply | 3.3V Power supply | Digital power supply for external IO | VDDE | | | | |
| | | SATA 3.3V analog power supply | ST_VDP | | | | |
| | | USB2.0 3.3V analog power supply | U2_AVDF1, U2_AVDB | | | | |
| Turning on/off the power supply | 1.2V Power supply | Analog power supply for PLL | PLLVDD | | | | |
| | | Digital power supply for internal core | VDDI | | | | |
| | | SATA 1.2V analog power supply | ST_VD, ST_VDU | | | | |
| | | USB3.0 1.2V analog power supply | SS_VDN、 SS_VDU | | | | |
| | | USB2.0 1.2V analog power supply | U2_AVDF2, U2_AVDP | | | | |

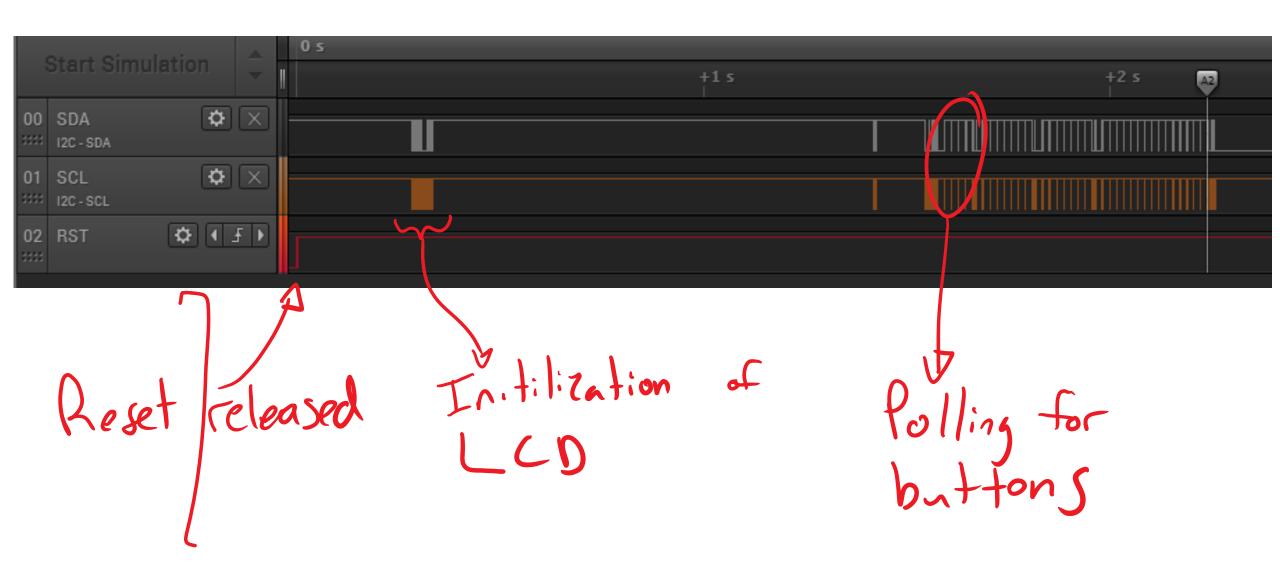


Can we byposs de lay?



1. Wait for I2C poll
2. Feed in button presses.
3. Check response.

https://github.com/colinoflynn/hddkeyboard-spoof-demo/



FINDINGS

- 1. Time-ont Not stored in NVM. La Can reset chip to bypass waits.
- 2. "Reasonable" entry speeds enforced Lp ~ 0.55 for 4 dig.ts
- 3. Boot involves about 1.50 delay.

KESULTS · 4 digit pin takes 2.23 s GUESS TIME (WORST CASE) LENGTH 6.2 hrs 2.6 days 26 days

260 days

You purchased this item on Dec 23 2015.

View this order

















Satechi LockDown USB 3.0 Super-Speed 256-bit Encrypted Portable External 2.5" SATA Hard Drive Enclosure by Satechi

★★★★ 13 customer reviews

Currently unavailable.

We don't know when or if this item will be back in stock.

- . Quality OLED screen enables easy password input, menu navigation, HDD info and more
- . Incorporates capacitance touch panel numeric keypad Unlock the drive with your own unique 4 to 8 digit password
- USB 3.0 Super-Speed (5Gbps) driver also supports USB 2.0, ARM based 32bit Controller
- Measures (W) 3.1 x (H) 5 x (D) 0.5 inches / 3.25 ounces (w/o HDD)





OK! HOW SECURE ARE THESE? Previous work by Czarny & Rigo:

8 Conclusion

Starting with no information, we managed, in full black box, to have a good understanding of the way this encrypted drive enclosure works.

While we know the crypto design is a fail, because all the encryption related data is stored on the drive itself, with no enclosure dependent secret, we were unable to actually exploit it.

SWAP IC FOR VIRGIN

-P Device worked.

LA [NO] fuse bits or other secret Spe cific to Manu facture.



Compare Bins

Two versions of program
for Zalman
drive.

(This is from (Zarny & Rigo)

```
zalman\fw\UE400_firmware_1-37(FAT).bin
     0010:
                                             C8 Ø1 A7 67
     0020:
                                             D1 ØE 18 68 5C
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F2 EC BC 7B 5F
7E A4 FC PA 67
D6 16 88 C2 19
                              ØC 38 8F
     0030:
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     0040:
                   0B D7 6D CB
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                       9E
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                              1C 6B BF
                                                            5F A1 6C
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                                                            67 00 18
     0070:
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                                                 ØB 76
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                44 4B 90
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               23 ED EB F4 46 ØF 15
               54 1F 5B DD 68 6D 07
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zalman\fw\UE400_firmware_1-37(NTFS).bin
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8A C3 32 F7 6E 21 38
F2 EC BC 7B 5F A1 6C
7E A4 FC F6 67 00 18
     0030:
                              ØC 38 8F
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     0040:
                F8 ØB D7 6D CB 4F 13
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     0050:
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                A4 5A
     0070:
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                                              D6 16 88
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                00 30 07
                              AC EC EA
                                              E9 DA 9C
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                                              3F 92 15 9F F2 CA 77
     00C0:
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                                  6C 6E
                                                                22 B4
                              32
                              46 ØF 15
                                          02 EF 45 0A 77 59 A3 9B
                                                                        è#ÝÙ¶F..
                                          F3 A3 77 AD
1000 01B0: 21 54 1F 5B DD 68 6D 07
                                                            99 8E 70
```

STREAM CIPHER

Keystream: E7 IA FI 37

B B B

Plantext: 20 IF 09 IA....

Ciphertext: CB 05 F8 2D

Compare (Sins File Length Flag? Codo -

No differnce in 9000 9070: 40 63 60 9000 9070: 40 63 60 9000 9070: 40 63 60 9000 9070: 40 63 60 9000 9070: 40 68 F2 9000 9070: 40 68 F2 9000 9070: 40 9000 9070: 40 68 F2 90 9000 9070: 40 9000 9070: 40 68 F2 90 9000 9070: 40 9000 9

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                                                             BB 52 38 62 0B 66 8F 1C
3A 01 87 DC C6 50 18 95
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       0140: E4 D3 84 85 86 FC 9F BD 0150: 76 2D D2 5C 5C F4 C E8 8160: 94 BD 16 44 49 C3 54 36
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                                                             D7 23 7E
                                 CB 7F C9 55 28
                                                             82 C6 E1 06 2B C6 22 B4
02 EF 45 0A 77 59 A3 9B
F3 A3 77 AD 73 99 8E 70
        0190: 53 8D CF 14 50 32 6C 6E
        01A0: 8A 23 ED EB F4 46 0F 15
       01B0: 21 54 1F 5B DD 68 6D 07
zalman\fw\UE400_firmware_1-37(NTFS).bin
        0010:
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         0020:
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                                                                  C8 Ø1 A7 67
                                                            AA C8 01 A7 67 D6 C1 18
D7 D1 0E 18 14 5C 21 D1
05 8A C3 32 F7 6E 21 38
04 F2 EC BC 7B 5F A1 6C
4F 7E A4 FC F6 67 00 18
F9 D6 16 88 36 1A 67 F1
B0 6F 0B 76 EA 3D 18 BA
6C 83 E1 7B A3 E6 26 E0
00 39 12 0A F5 19 DF E0
A5 E9 DA 9C BA 02 0E 55
B3 3F 92 15 9F F2 CA 75
88 14 2E 49 3E 22 F5 05
7C 7A BB CD C3 19 10 7F
                       4D 92 1B 54 0C 38 8F
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C1 36 9E 26 1C 6B BF
A4 5A B1 13 1B 80 E2
63 63 D0 83 D1 E3 9A
77 7F A4 3D 75 3D A9
F2 42 55 F8 CØ 91 A8
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                                  EB F4 46 0F 15
                                                             02 EF 45 0A 77 59 A3 9B
                                                             F3 A3 77 AD
                                                                                       99 8E 70
```

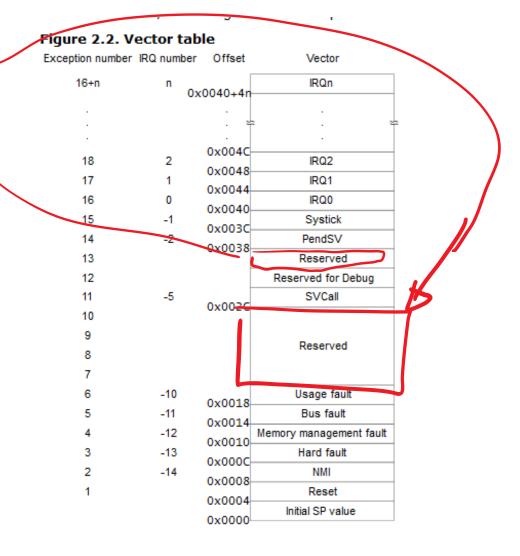
Zalman

```
zalman\fw\UE400_firmware_1-37(FAT).bin
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     0010:
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                                               87
                                                   DC C6 50 18
                                                                95
                                                                        ±0ú0±
                                32 AØ
                                                  5A
                      EØ
                             83
                                        19
                  75
                                           3D
                                               46
                                                         44
                                                                     ï.uó.â2á
               DØ 84
                      89
                             FC
                                9B BD
                                        FA
                                               F1
                                                   BE
                                                      C5
                                                         79
                                                                C4
                                                                     őðäëå³ø¢
                                4C E8
                  D2
                      5C
                             F4
                                        24
                                               93
                                                   CB
                                                      12 B1
                                                                04
                          SC.
                                            83
                                                             18
                                                                     û-Ê\\¶LÞ
                            C3
                               54 36
                                        76 A6
           94
               BD 16
                      44
                          49
                                               4A D1 5D
                                                         4C BE EØ
                                                                     ö¢.DI |T6 v≗JÐ]L¥ó
0000 0170: FF 60 7D 96 D3 DD 9C C7
                                                                       }ûë l£ã
                                        9A 69 CØ 6Ø C7 7F EB 8F
0000 0180:
0000 0190:
```

```
zalman\fw\VE400 firmware 1-37(FAT).bin
0000 0000:
0000
     0010:
                                bb Br
                                                           00
                                                              F1
     0070:
                                3D
                                                               BA
                                                               EØ
                                                                     BU
     0090:
                                                                     KÉ
                                                               EØ
     имай:
                                   32
                                   ΕA
     00B0:
                                EC
0000
     ииси:
                                85
                                   41
0000
     00D0:
                                                                   û®±:ô#LQ
                                4C
                                  51
                                                               7F
     иини:
                                              10 D5 5B
                                  D1
                                              DF 95
                                                    EC
                                                                             .r⊸òý¦s7
                                                                             nR8T
                  A9
                         89
                                              38
                                                               DC
     0110:
                            A1
                         9B 97
    0120:
                      D6
                                4F F1
                                                        50 18
     0130:
                         17 83 32 A0
               ØE
                  75
                     EØ.
                                              46
                                                 5A
                                                    DC
                                                        44
                                                          - 88
                                                              DF
                                                                             .=FZ_Dê_
     0140:
            E4
               DØ
                  84
                         86 FC 9B BD
                                       FA
                                                        79
                                                           EF
                                                              C4
                                                                   őðäëå³ø¢
                                                                             $âôπ.....
     0150:
               2D D2 5C 5C F4 4C E8
                                       24
                                                    12
                                                        B1
                                                           18
                                                              04
                               54 36
                         49
                            C3
                                       76 A6
                                                 D1 5D 4C BE
                                                              EØ
                                                                   ö¢.DI ⊦T6
     0160:
            94
               BD
                  16 44
                     96 D3
                            DD 9C C7
                                                    C7 7F
                                                                     >û릣ã Üi∟`ã△ùA
     0170:
               60 70
                                       9A 69
                                                 60
                                                              -8F
     0180:
     0190:
                     14
```

Blocks 8, 9, 10, 11 same (Block=32 b.1s)

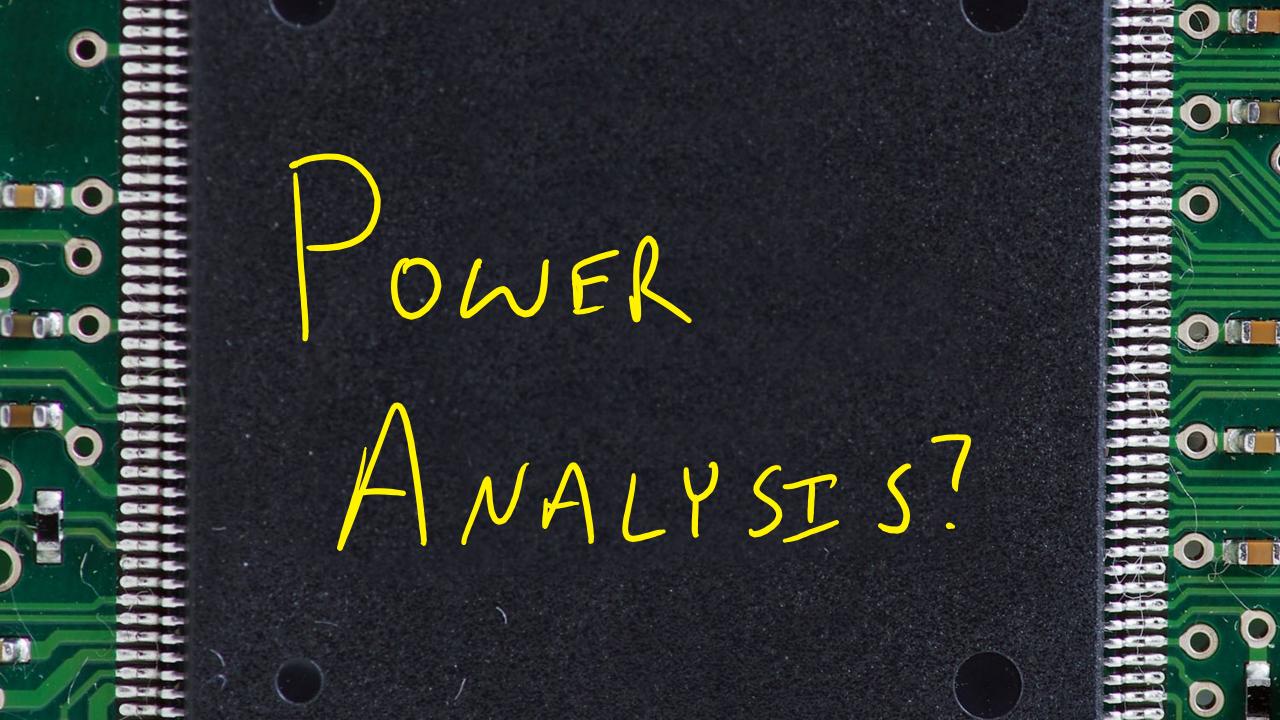




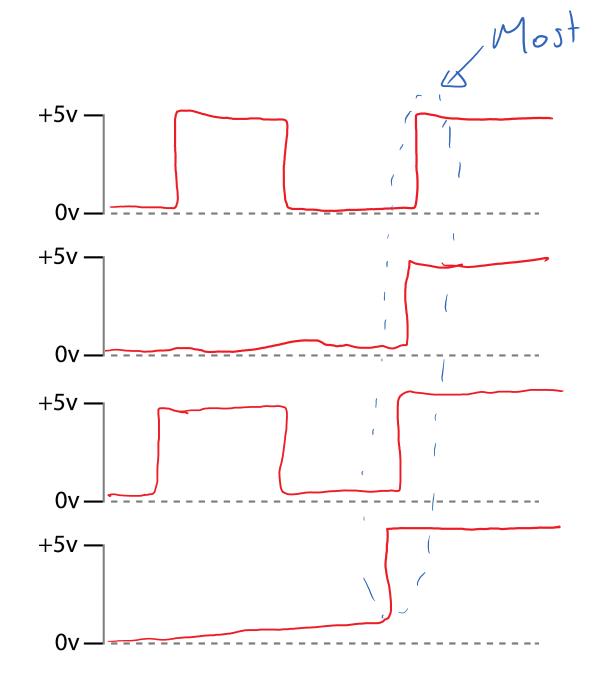
On system reset, the vector table is fixed at address 0x00000000.

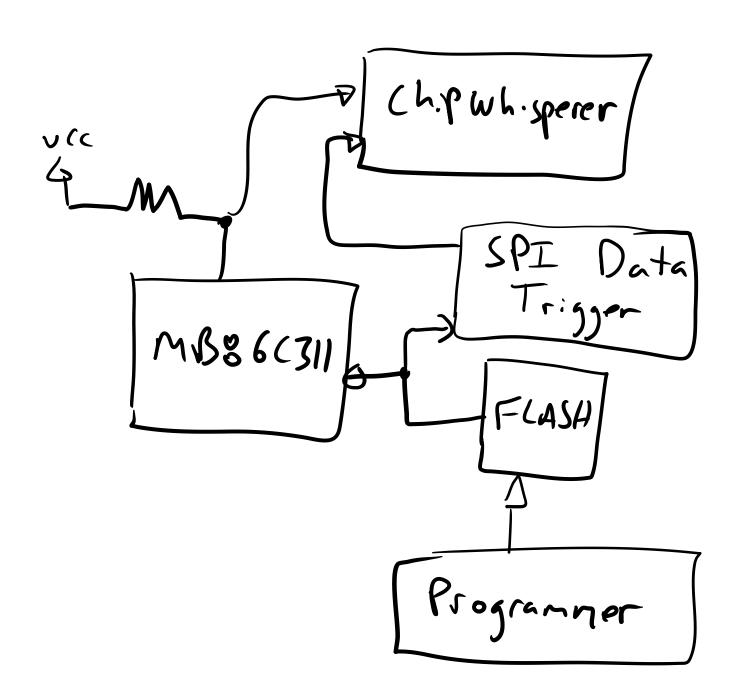
Knowledge?

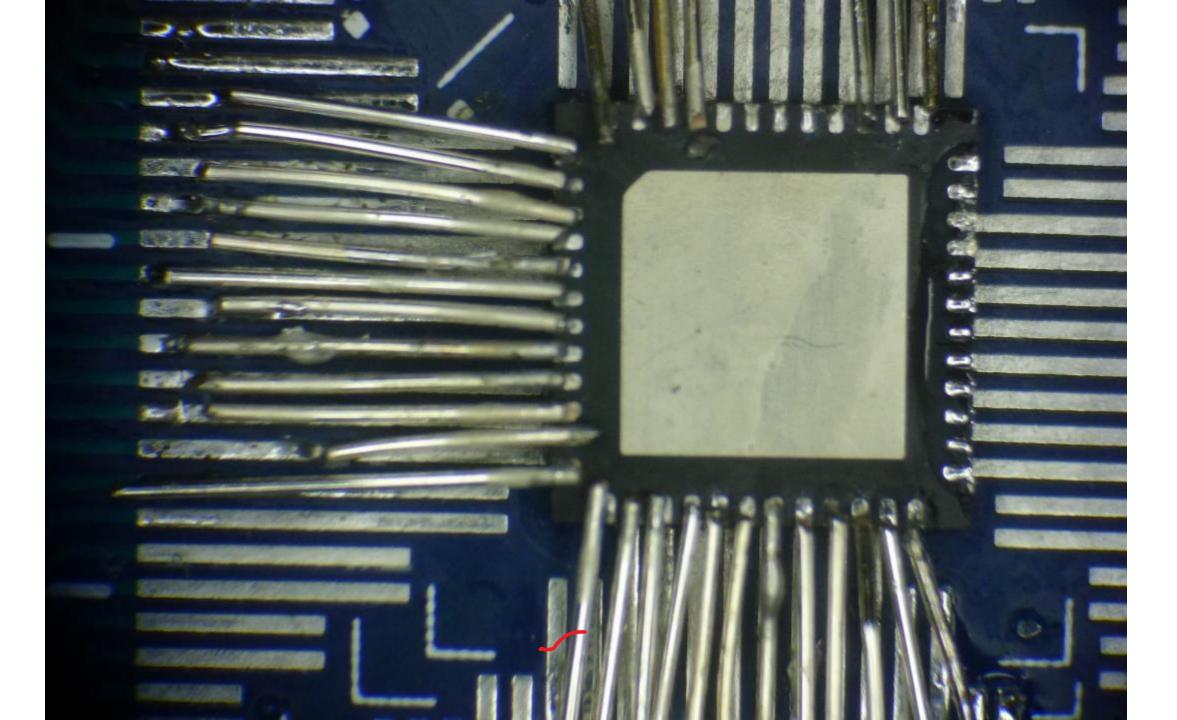
- Direct ARM Cortex M3 code, with Stream cipher.
- Break strem cipher, all is lost.

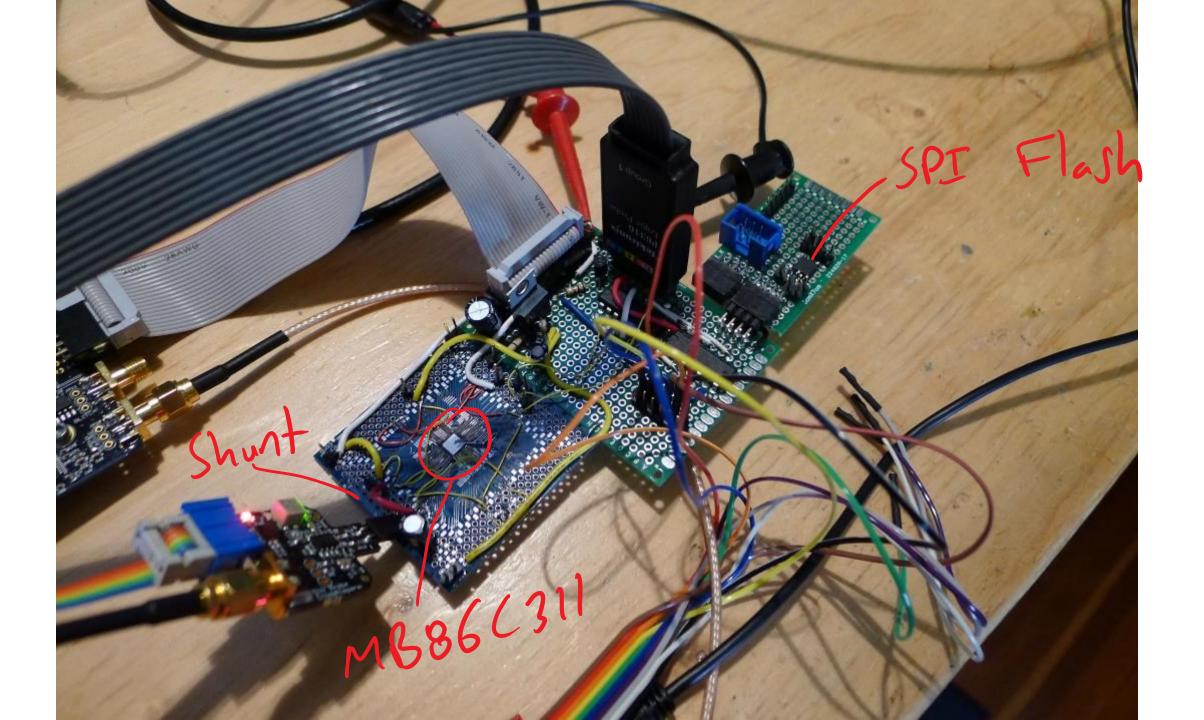


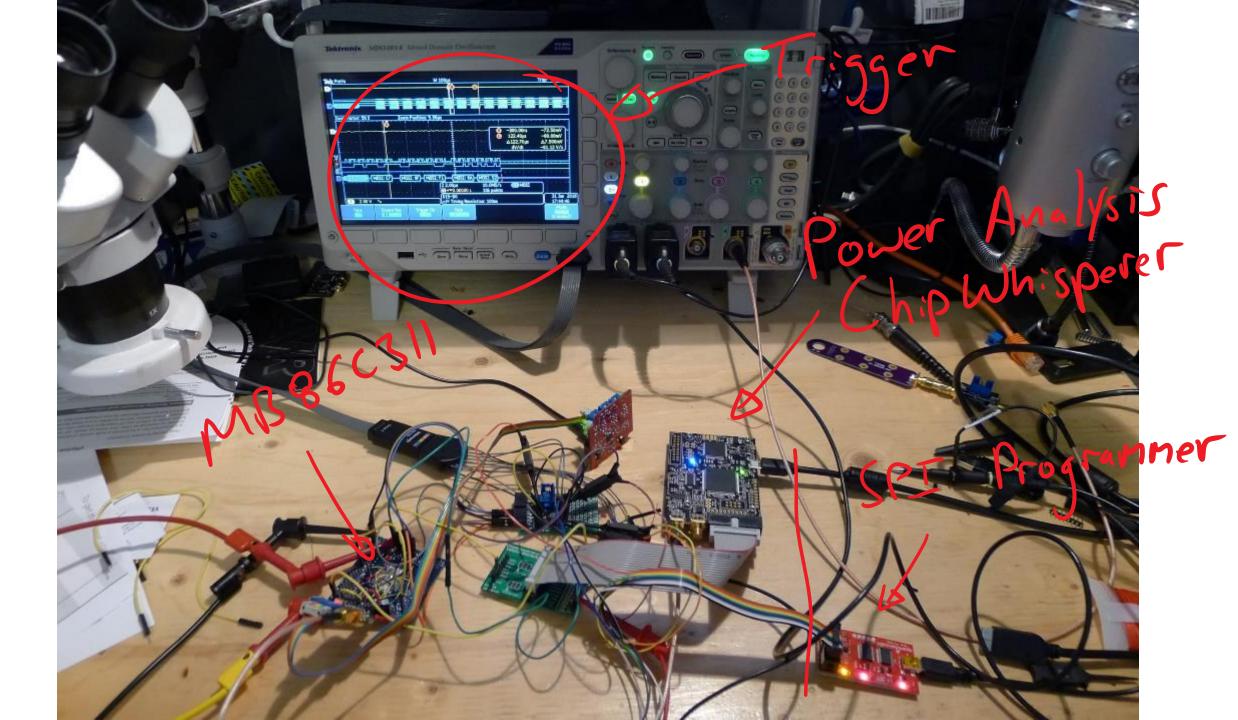
Data Busses... +5v¬



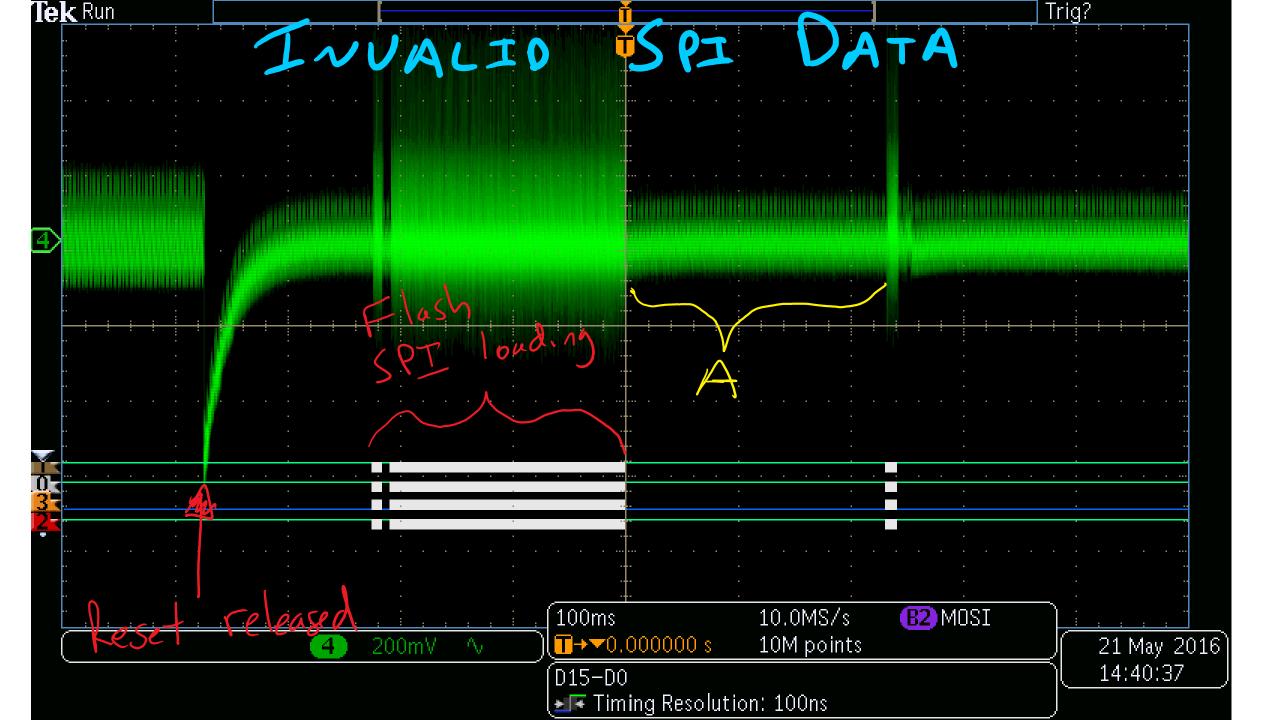


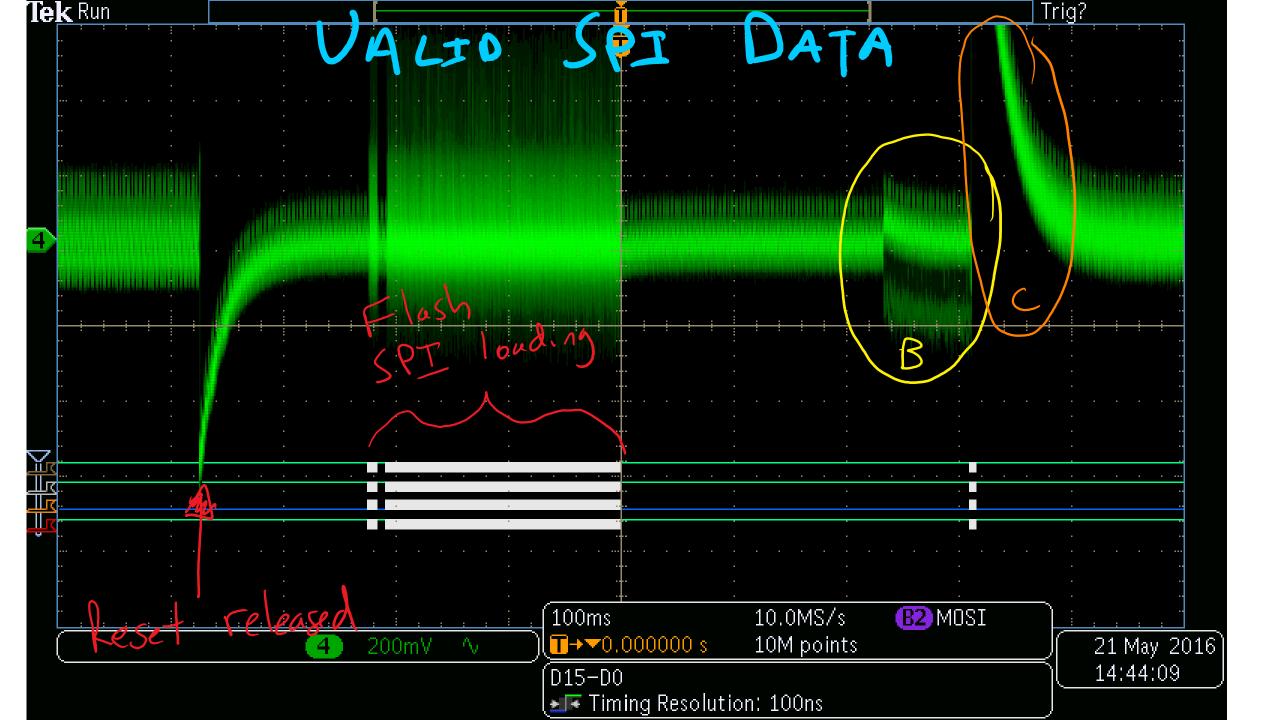






DOWER ANALYSIS 1. Program SPI Flash with pattern. 2. Reset MB86711, will reload FLASH. 3. Capture power at different places





Comparison

Invalid = Changed a few bytes

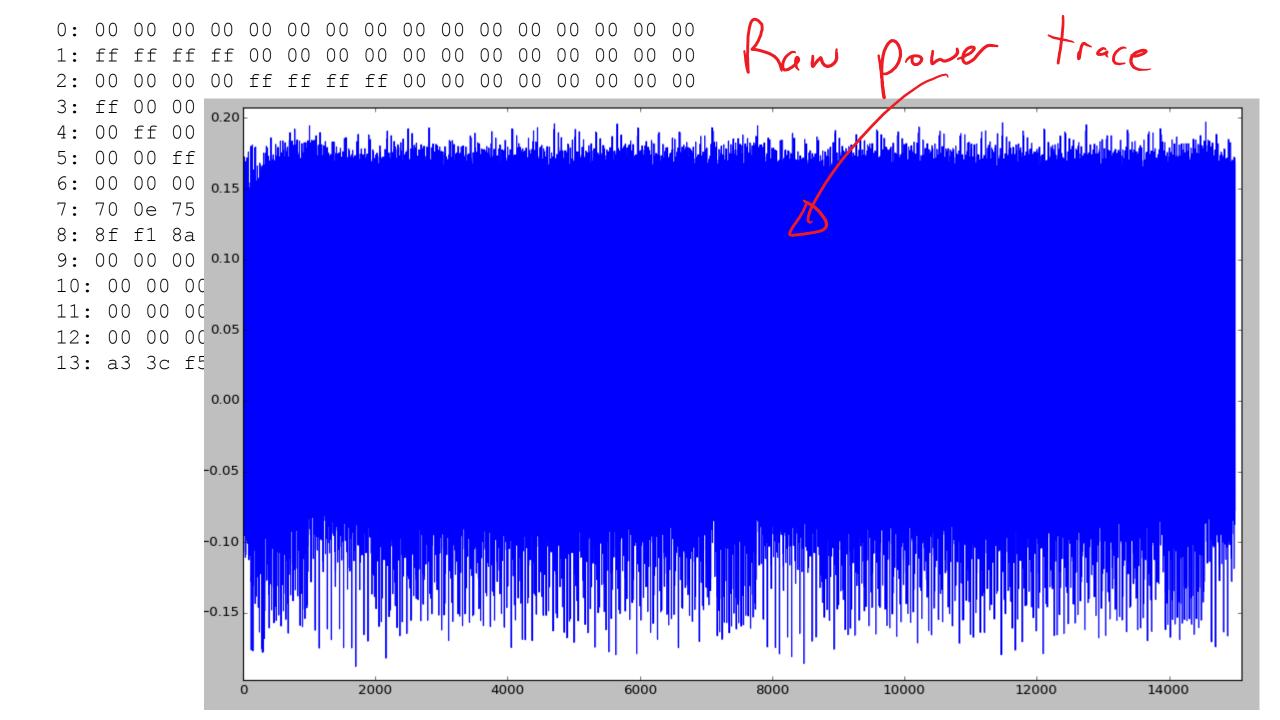
Varid = Original Image 1) Both complete operation "A" 2) Only valid code completes "B" (3) Assume "C" is switching to operating code, maybe powering on SATA etc?

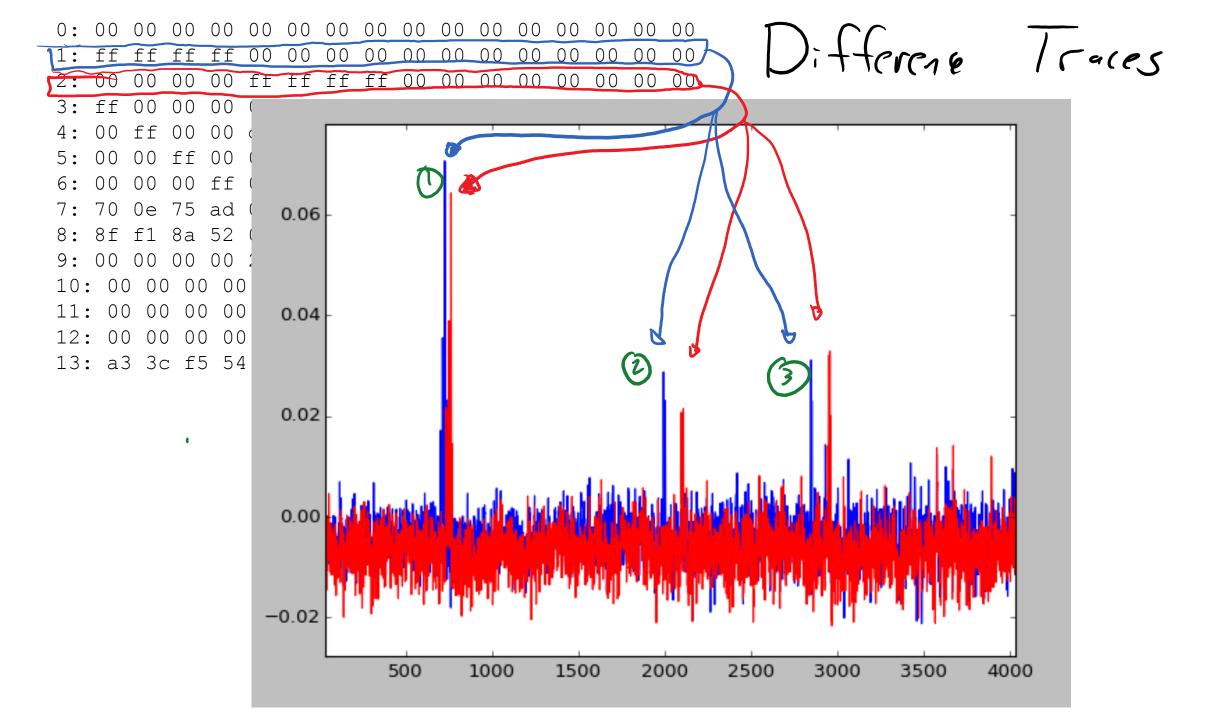
LEARNINGS

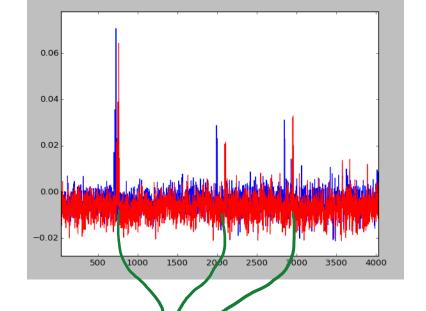
As (A) + B (B) when code valid
When code invalid

(A) = Validation (Hash?)



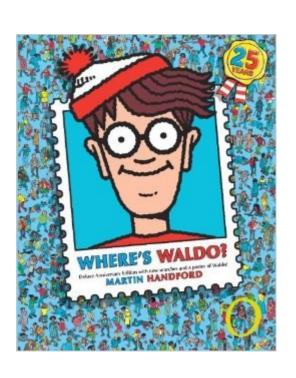






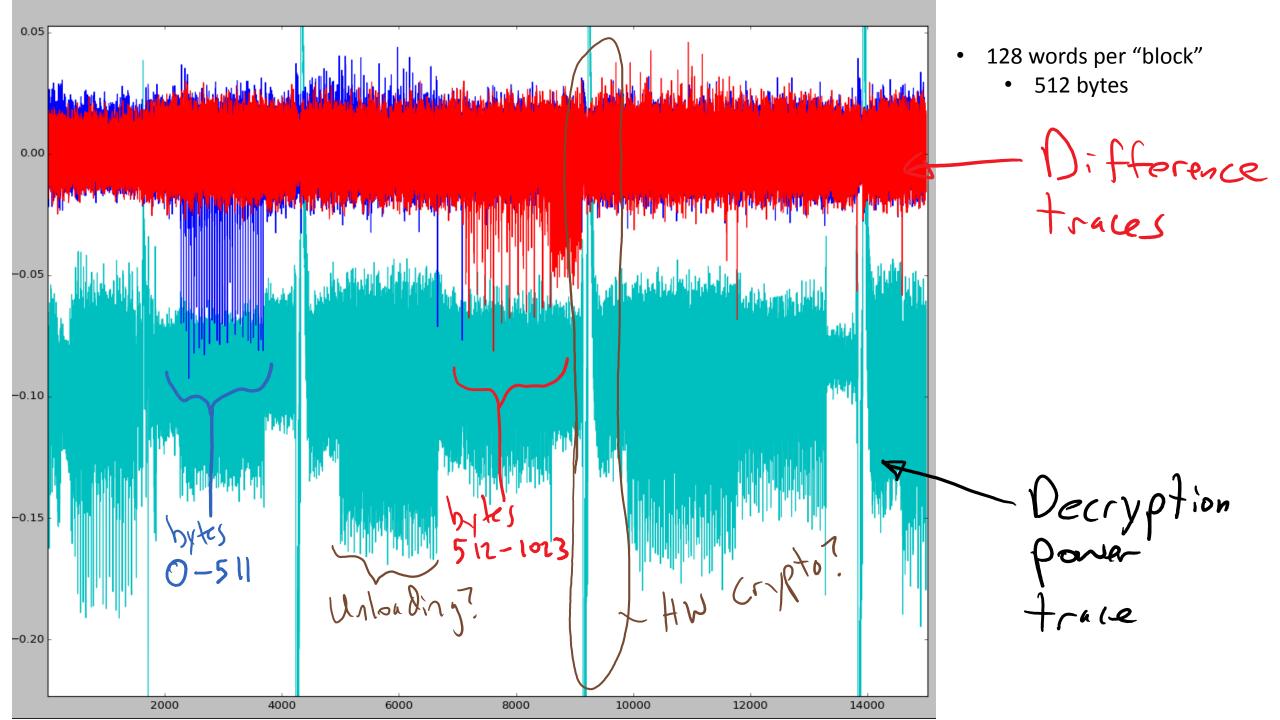
Multiple Manipulations La Some sort of feedback?

FINDING HASH



```
#Simple python script to try a bunch of hashes
 import hashlib
 raw data = open('firmware_C311.bin', 'rb').read()
 known hash = raw data[0:32]
 test hash = hashlib.algorithms available
☐ for startoffset in range(0x20, 0x28, 4):
     #Trim bytes off end, seems to be some header at end?
     for endoffset in range(0, -1024, -1):
         firmware = raw_data[startoffset:endoffset]
         for t in test_hash:
             h = hashlib.new(t)
             h.update(firmware)
             dig = h.hexdigest()
             #This example uses known hash, NOT read from start of file
             #If searching in middle, do:
             #if "77badb" in dig:
             if dig.startswith("77badb"):
                 print dig
                 print "***HASH FOUND?***"
                  print "start=0x%02x, end=0x%02x, type=%s"%(startoffset, endoffset, t)
```

```
import hashlib
                                              BUILD SIGNATURES
fd = open('lockdown_hdd1.bin', 'rb')
orig_flash = fd.read()
fd.close()
#As an example - write 0xff's in blanks
programmed file = [0xff]*16
newdata = orig flash[:]
newdata = [ord(d) for d in newdata]
for i in range(0x2044, 0x2044+16):
    newdata[i] = programmed file[i-0x2044]
#Calculate new hash
newdata temp = ''.join([chr(j) for j in newdata])
h = hashlib.new("sha256")
h.update(newdata temp[0x2020:0x170F8])
shash = h.hexdigest()
#Add hash to file
i=0
for t in range(0, len(shash), 2):
    newdata[0\times2000+i] = int(shash[t:(t+2)], 16)
    i += 1
#Save file
newdata = ''.join([chr(j) for j in newdata])
fd = open('lockdown temp.bin', 'wb')
fd.write(newdata)
fd.close()
```



FIRMWARE ATTACKS 1) Can cause arbitrary code to be decrypted. 2) Can move interrupt/reset vectors quazi-blindly. 3) If could determine either (a) part of stream cipher or (b) part of code (init routines?) may be able to build short "dum nor" "dun per"

HARD DRIVE SUMMARY 1. No secrets inside silicon 2. Secure and normal varients of.
MB86C311Use same FW protection. ... Any company using these devices could decrypt this FW.

HARD DRIVE SUMMARY 3. Once F.W. is known, all is lost. Possible to brute-force pass word in <15.

4. Various attack vectors to FW itself due to flows.



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