

ECED2200 – DIGITAL CIRCUITS

Sequential Logic

GENERAL NOTES

- See updates to these slides: www.newae.com/teaching
- These slides licensed under ‘[Creative Commons Attribution-ShareAlike 3.0 Unported License](https://creativecommons.org/licenses/by-sa/3.0/)’
- These slides are not the complete course – they are extended in-class
- You will find the following references useful, see www.newae.com/teaching for more information/links:
 - The book “Bebop to the Boolean Boogie” which is available to Dalhousie Students
 - Course notes (covers almost everything we will discuss in class)
 - Various websites such as e.g.: www.play-hookey.com
 - The book “Contemporary Logic Design”, which was used in previous iterations of the class and you may have already

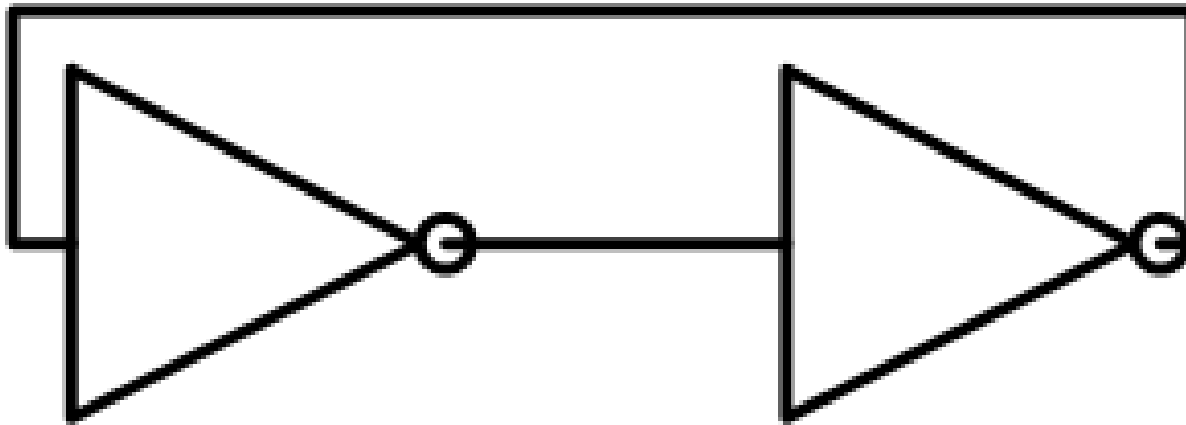
COMBINATIONAL LOGIC

SEQUENTIAL LOGIC

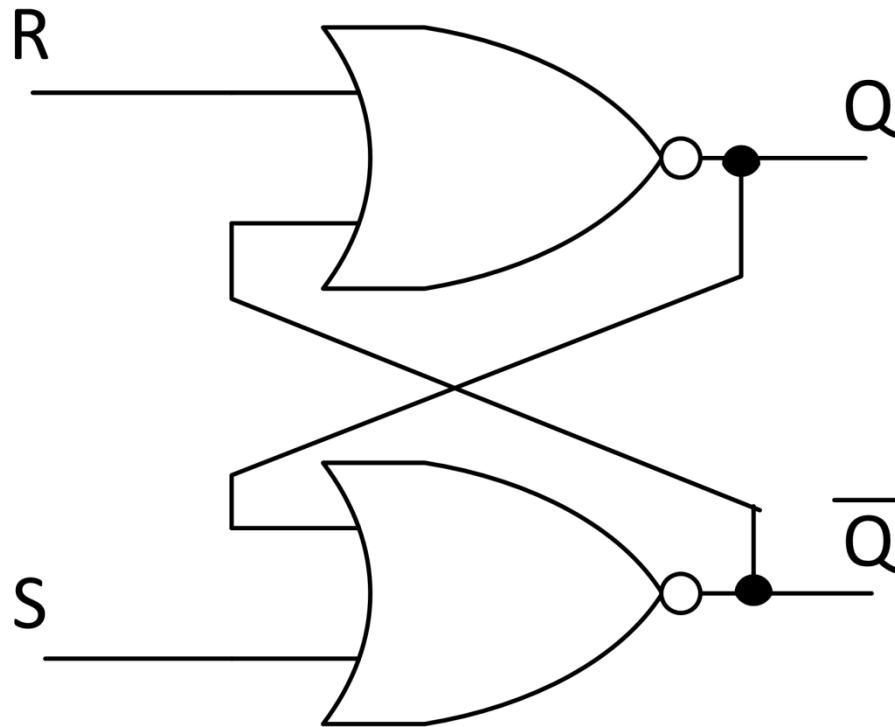
COMBINATIONAL VS SEQUENTIAL

- Output of combinational logic depends **ONLY** on current inputs
- Output of sequential logic depends on internal state

BASIC STATE STORAGE ELEMENT

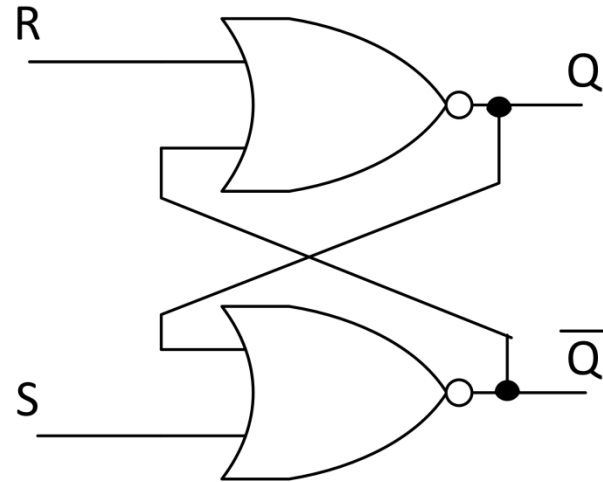


RESET / SET (RS) LATCH



RS LATCH EXPERIMENTS

Q^-	S	R	\overline{Q}^+	Q^+
0	0	0	1	0
0	1	0		
	1	0		
	0	0		
	1	0		
	0	0		
	0	1		
	0	1		
	0	0		
	1	1		

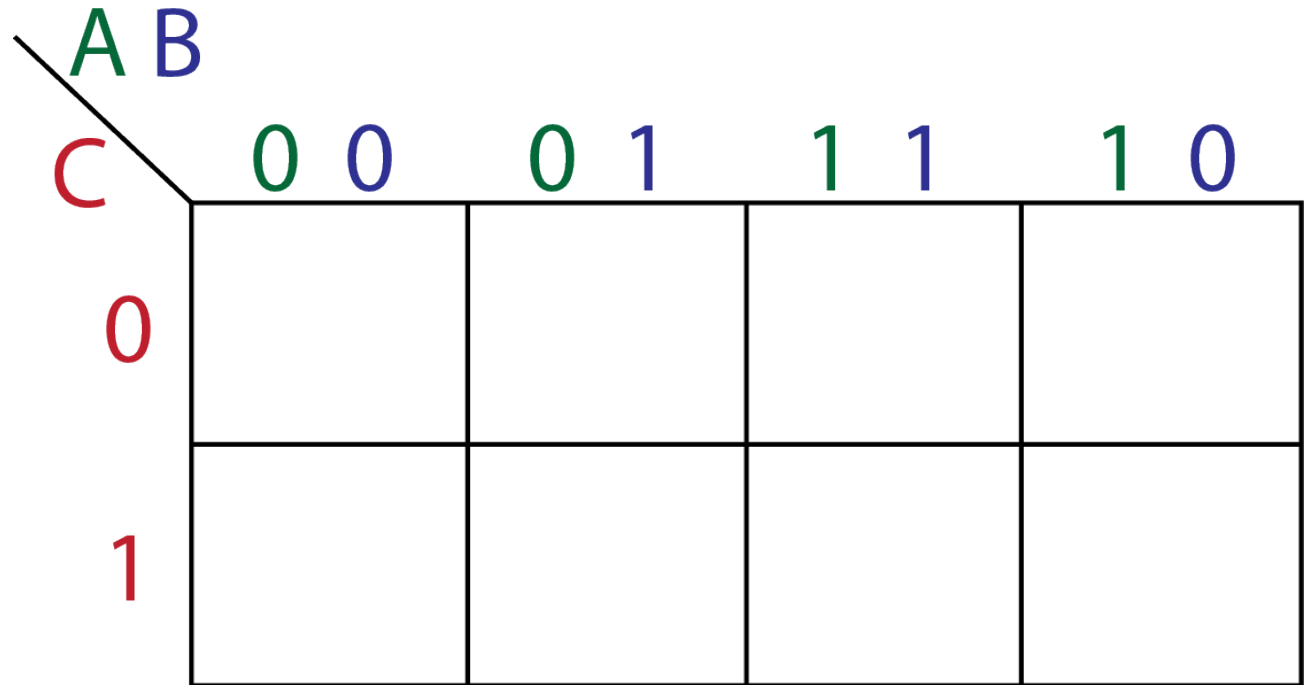


RS LATCH TRUTH TABLE

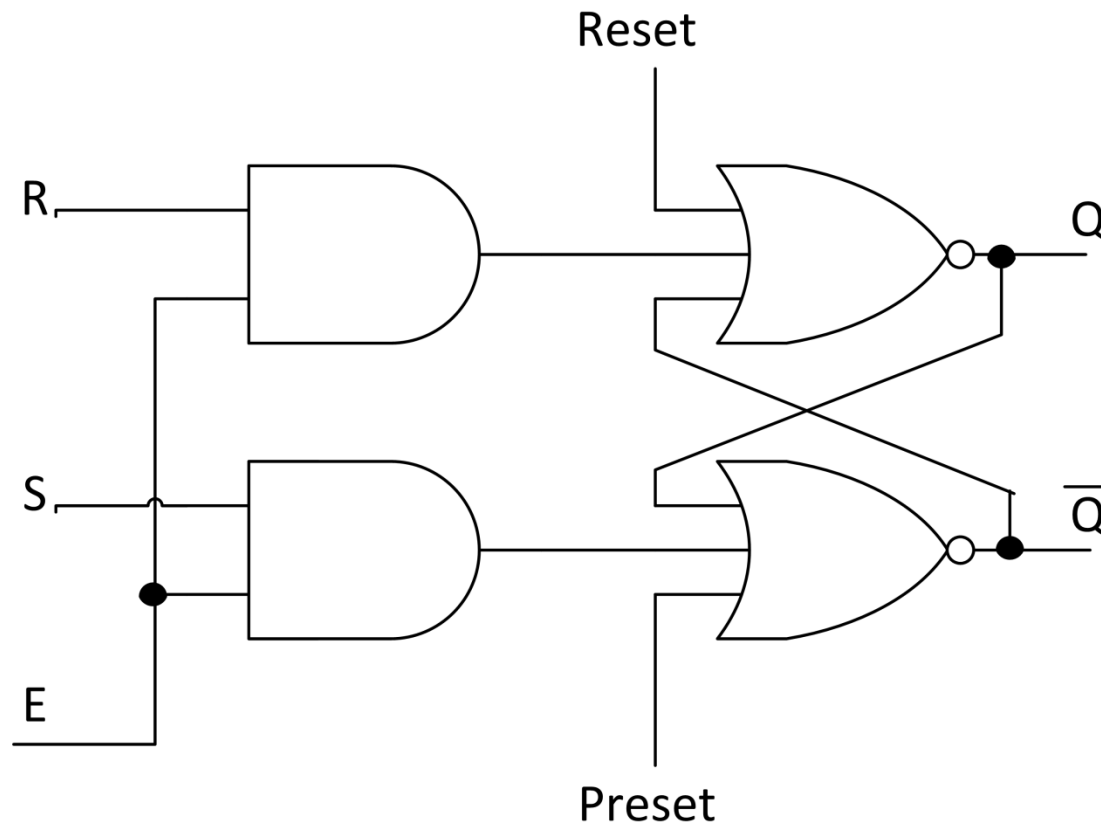
S	R	Q	Q ⁺	
0	0	0	0	Hold
0	0	1	1	
0	1	0	0	Reset
0	1	1	0	
1	0	0	1	Set
1	0	1	1	
1	1	0	X	???
1	1	1	X	

EQUATION OF RS LATCH

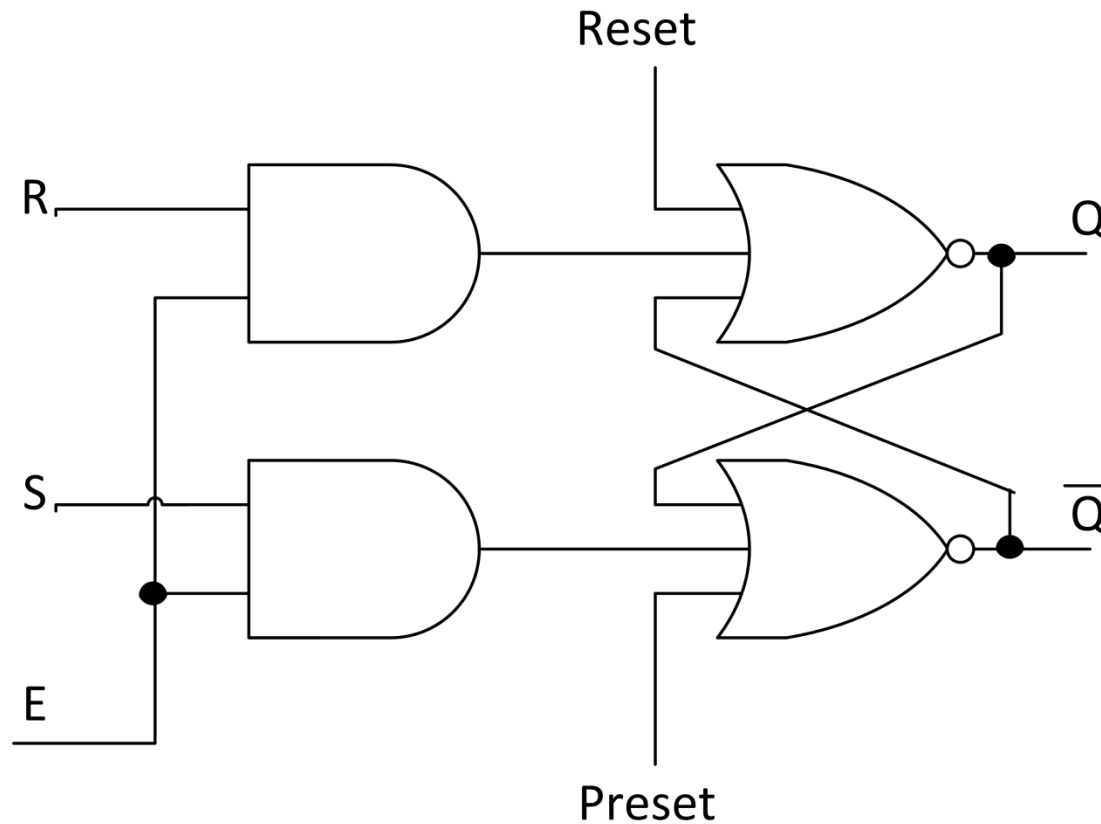
R	S	Q	Q ⁺
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	X
1	1	1	X



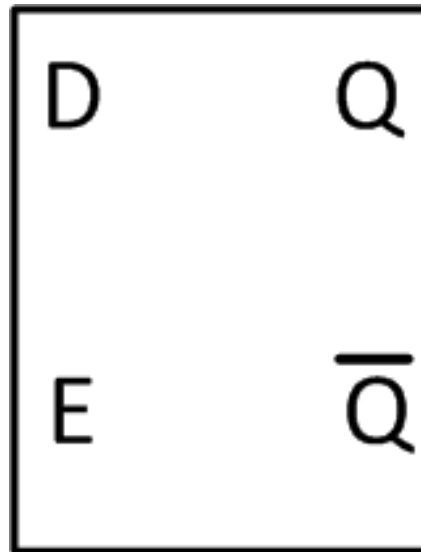
ADDITIONAL INPUTS



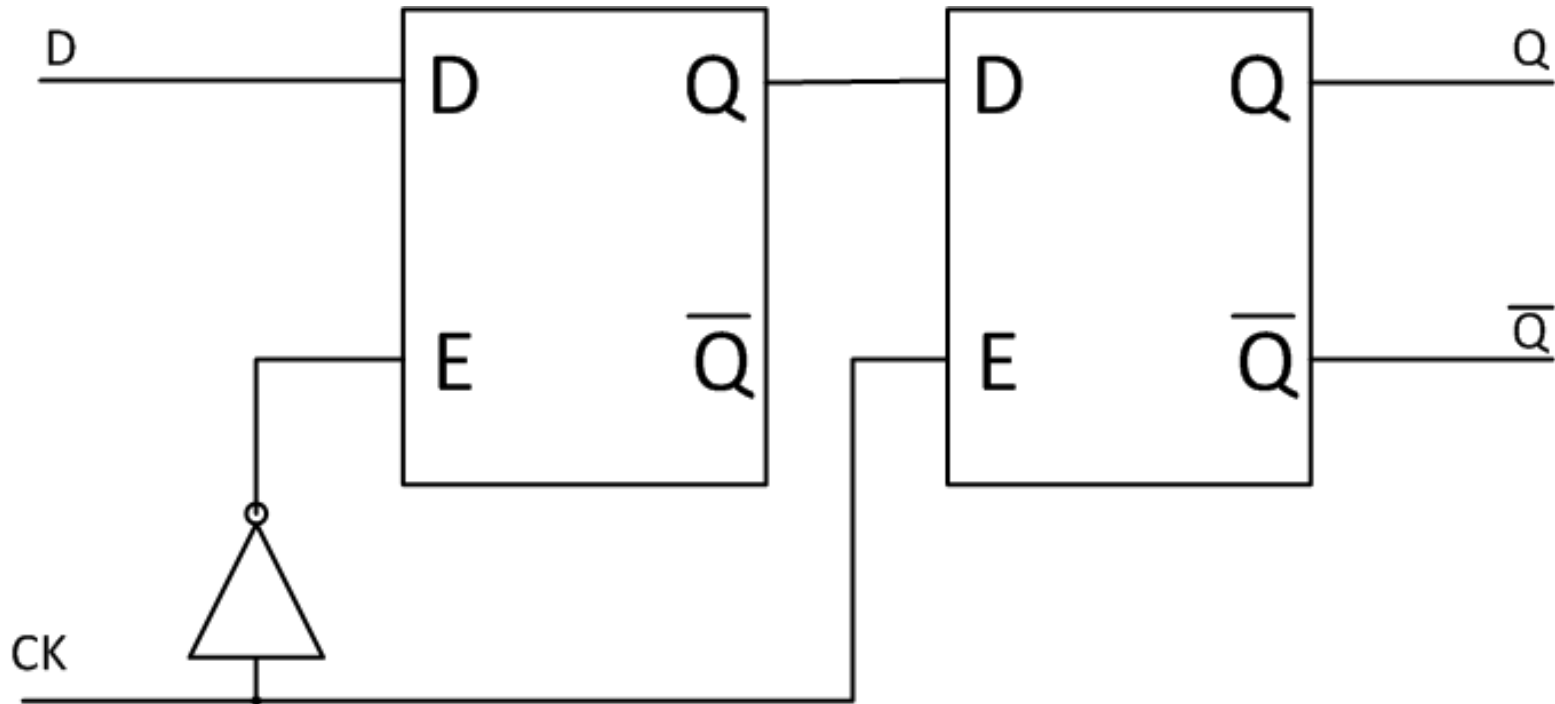
DATA LATCH



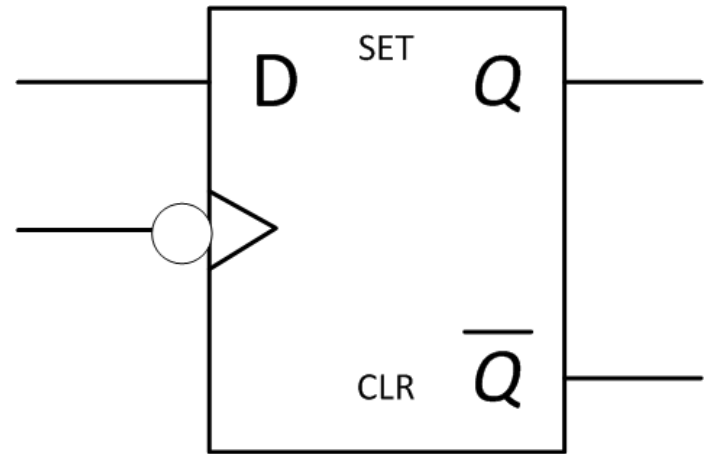
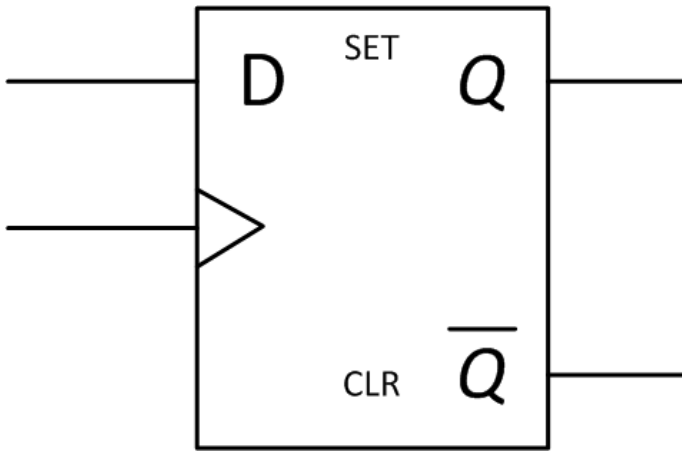
DATA LATCH SYMBOL



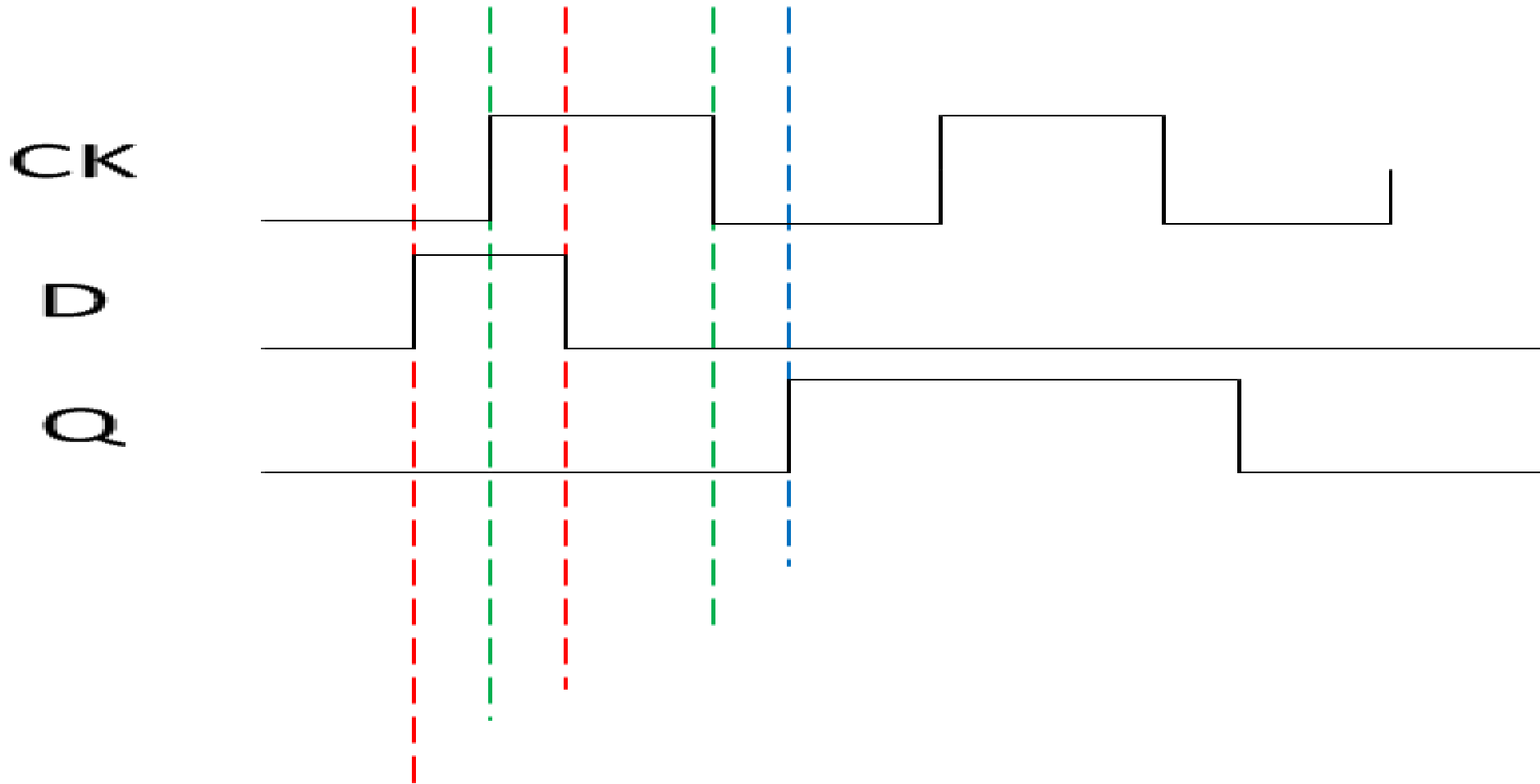
D FLIP-FLOP



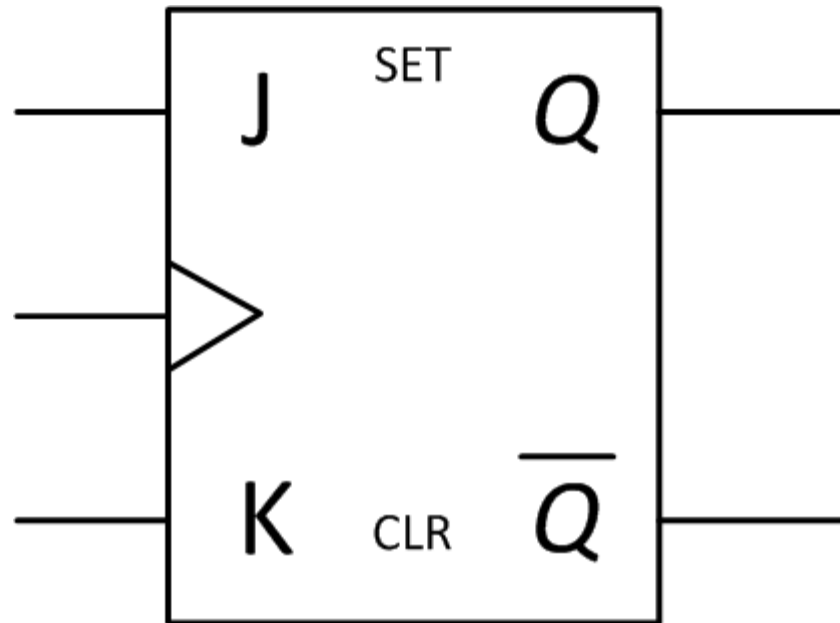
FLIP-FLOPS



TIMING IN FLIP-FLOPS



THE JK FLIP FLOP

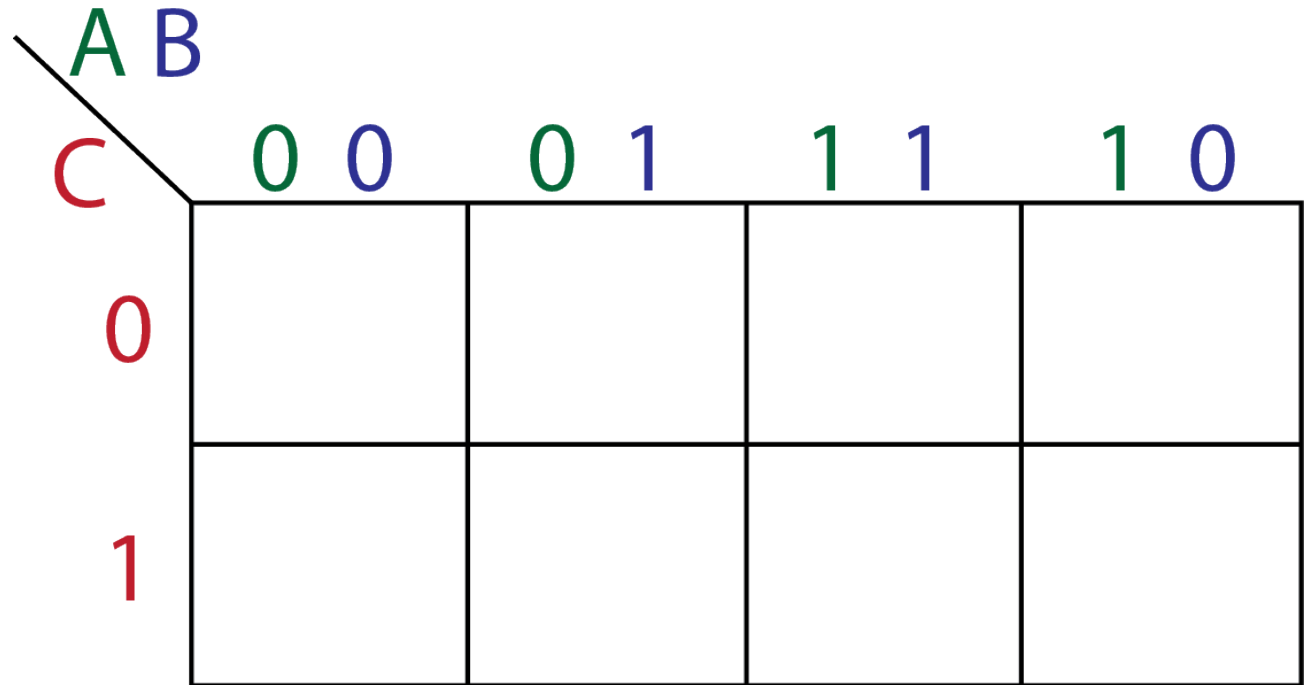


JK FLIP FLOP TRUTH TABLE

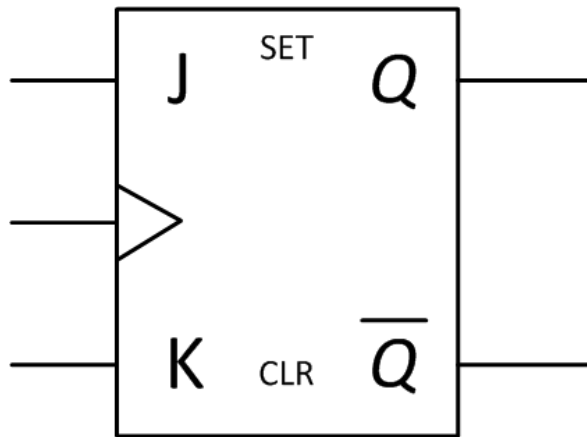
J	K	Q	Q ⁺	
0	0	0	0	Hold
0	0	1	1	
0	1	0	0	Reset
0	1	1	0	
1	0	0	1	Set
1	0	1	1	
1	1	0	1	Toggle
1	1	1	0	

EQUATION OF JK FLIP-FLOP

J	K	Q	Q ⁺
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

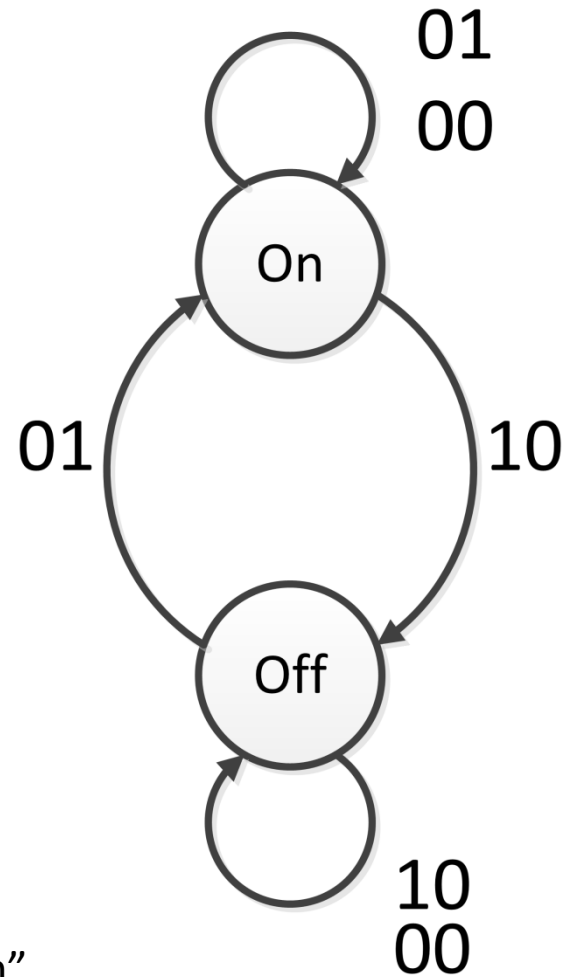


TOGGLE (T) FLIP FLOP



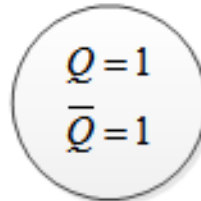
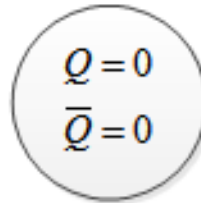
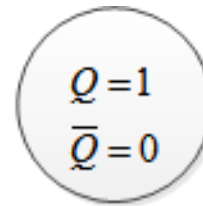
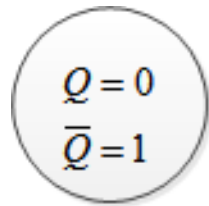
DESCRIBING FF STATE TRANSITIONS

STATE DIAGRAM



State = "Off Button" "On Button"
e.g.: 01 = on button pressed

RS STATE DIAGRAM



STATE TRANSITION TABLE - RS

Q	Q ⁺		
0	0		
0	1		
1	0		
1	1		

STATE TRANSITION TABLE - RS

Q	Q ⁺	R	S
0	0	?	0
0	1	0	1
1	0	1	0
1	1	0	?

STATE TRANSITION TABLE - D

Q	Q ⁺		
0	0		
0	1		
1	0		
1	1		

STATE TRANSITION TABLE - D

Q	Q ⁺	D	
0	0	0	
0	1	1	
1	0	0	
1	1	1	

STATE TRANSITION TABLE - JK

Q	Q ⁺		
0	0		
0	1		
1	0		
1	1		

STATE TRANSITION TABLE - JK

Q	Q ⁺	J	K
0	0	0	?
0	1	1	?
1	0	?	1
1	1	?	0

STATE TRANSITION TABLE - T

Q	Q ⁺		
0	0		
0	1		
1	0		
1	1		

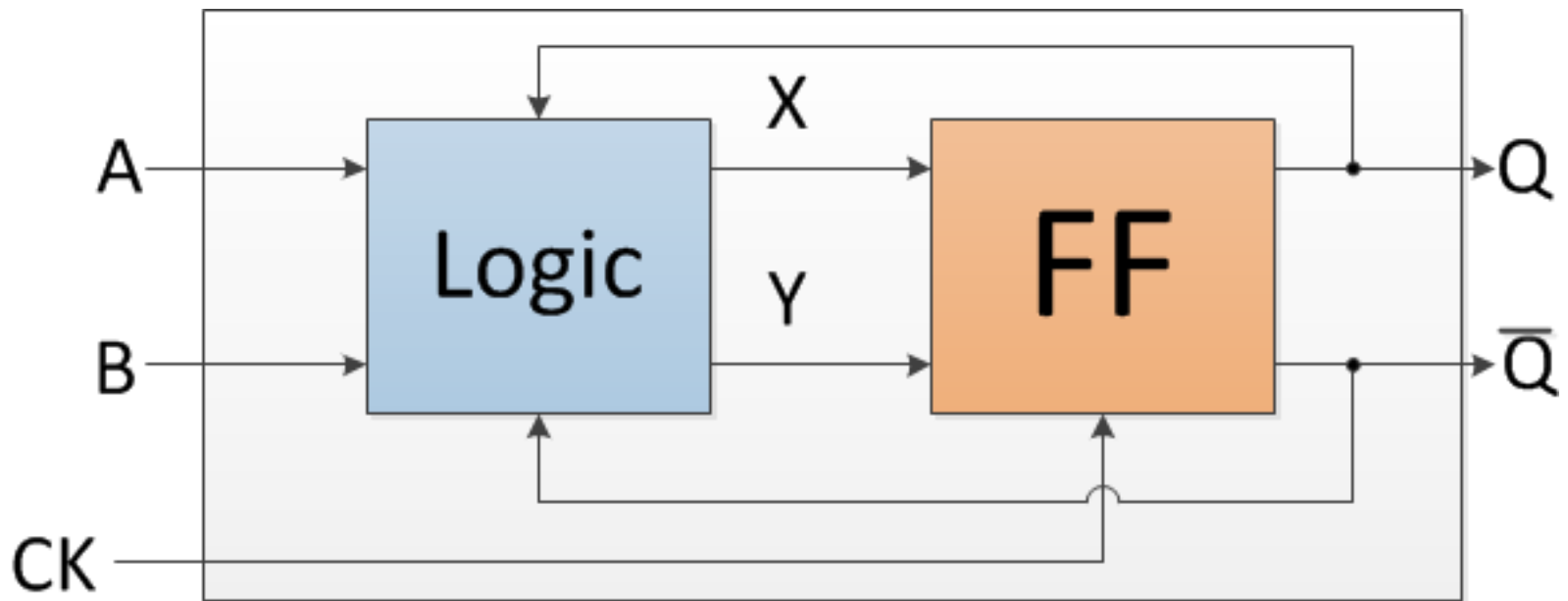
STATE TRANSITION TABLE - T

Q	Q ⁺	T	
0	0	0	
0	1	1	
1	0	1	
1	1	0	

STATE TRANSITION TABLE - MASTER

Q	Q ⁺	R	S	D	J	K	T
0	0	?	0	0	0	?	0
0	1	0	1	1	1	?	1
1	0	1	0	0	?	1	1
1	1	0	?	1	?	0	0

CONVERSION OF FF TYPES



EXAMPLE: JK WITH D

Q	Q ⁺	D	J	K
0	0	0	0	?
0	1	1	1	?
1	0	0	?	1
1	1	1	?	0

EXAMPLE: JK WITH D

Q	Q ⁺	D	J	K
0	0	0	0	?
0	1	1	1	?
1	0	0	?	1
1	1	1	?	0

Q	D	J	K
0	0	0	?
0	1	1	?
1	0	?	1
1	1	?	0

Q	J	K	D
0	0	?	0
0	1	?	1
1	?	1	0
1	?	0	1

EXAMPLE: JK WITH D

Q	J	K	D
0	0	?	0
0	1	?	1
1	?	1	0
1	?	0	1

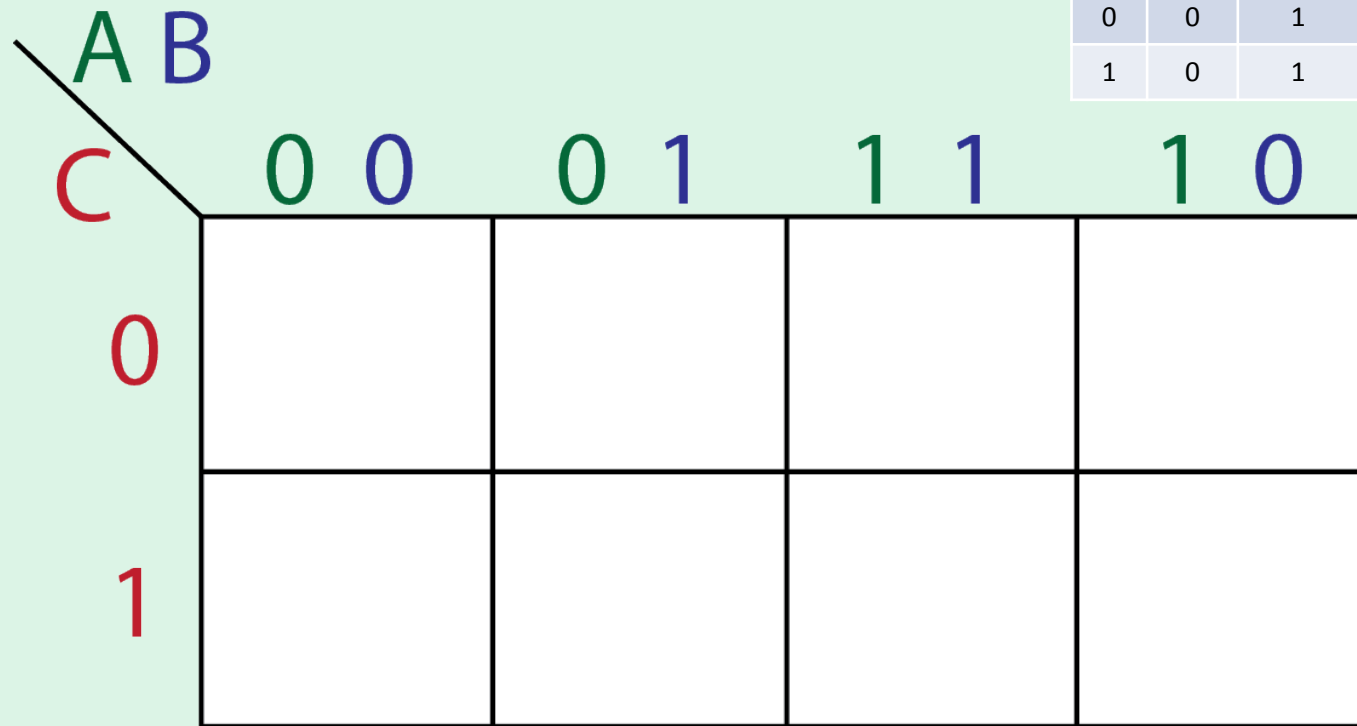
J	K	Q	D (out)
0	?	0	0
1	?	0	1
?	1	1	0
?	0	1	1

EXAMPLE: JK WITH D

J	K	Q	D (out)
0	0	0	0
0	1	0	0
1	0	0	1
1	1	0	1
0	1	1	0
1	1	1	0
0	0	1	1
1	0	1	1

EXAMPLE: JK WITH D

J	K	Q	D (out)
0	0	0	0
0	1	0	0
1	0	0	1
1	1	0	1
0	1	1	0
1	1	1	0
0	0	1	1
1	0	1	1



$$D = J \cdot \bar{Q} + \bar{K} \cdot Q$$

EXAMPLE: JK WITH D

EXAMPLE: D WITH JK

Q	Q ⁺	D	J	K
0	0	0	0	?
0	1	1	1	?
1	0	0	?	1
1	1	1	?	0

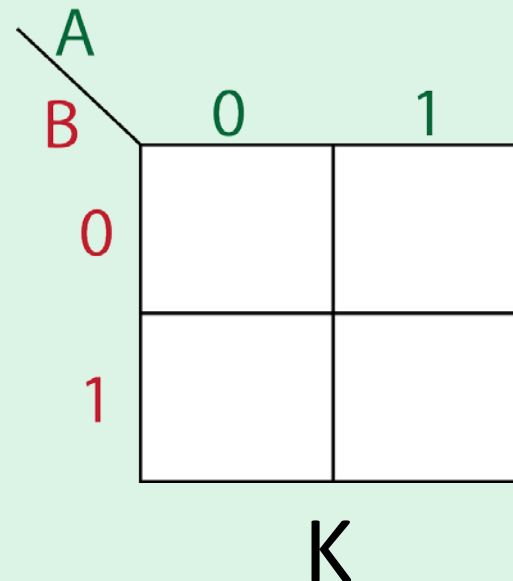
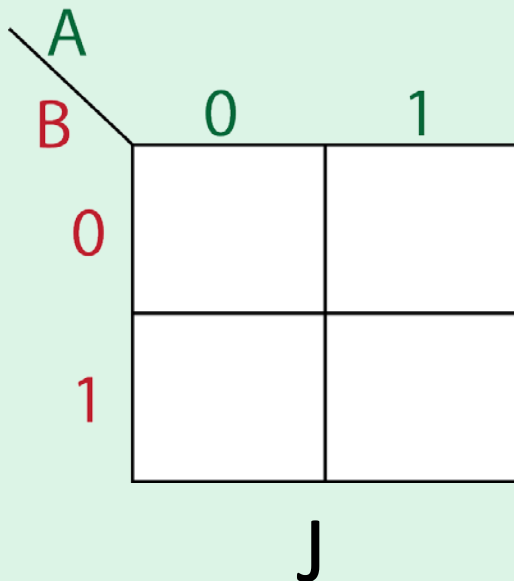
EXAMPLE: D WITH JK

Q	Q ⁺	D	J	K
0	0	0	0	?
0	1	1	1	?
1	0	0	?	1
1	1	1	?	0

Q	D	J	K
0	0	0	?
0	1	1	?
1	0	?	1
1	1	?	0

EXAMPLE: D WITH JK

D	Q	J (out)	K (out)
0	0	0	?
1	0	1	?
0	1	?	1
1	1	?	0



EXAMPLE: D WITH JK

FF CONVERSION PROCESS

1. Draw out a truth table where the input(s) are your input(s) for the FF you are creating + current state Q , and the output(s) are the required inputs to the FF you have
2. Fill out the outputs based on the state transition table
3. Move to a K-Map
4. Create logic expressions

SUMMARY

ECED Notes “Sequential Logic Design”

Bebop to the Boolean Boogie Chapter 11